

Figure 1

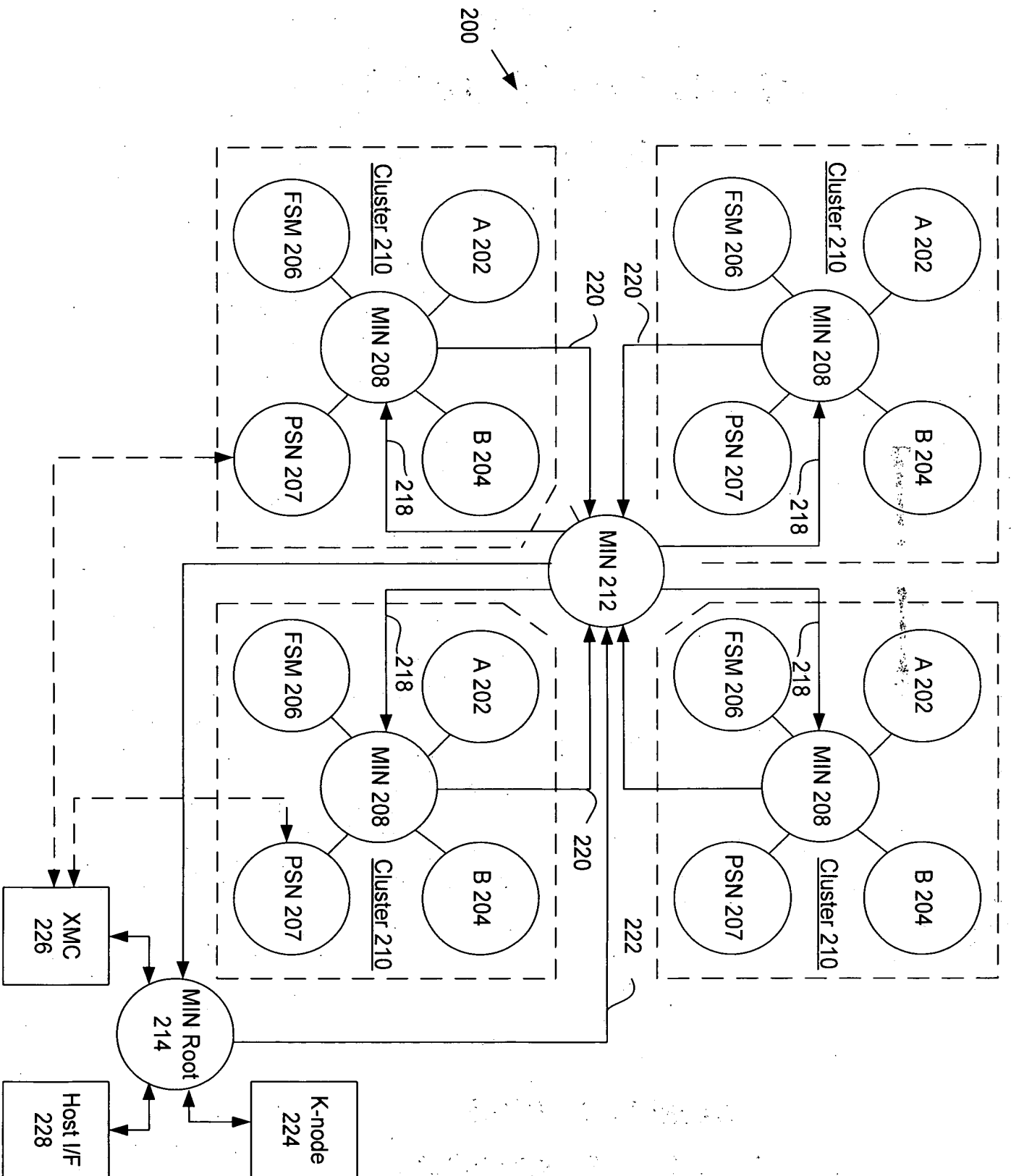


FIGURE 2

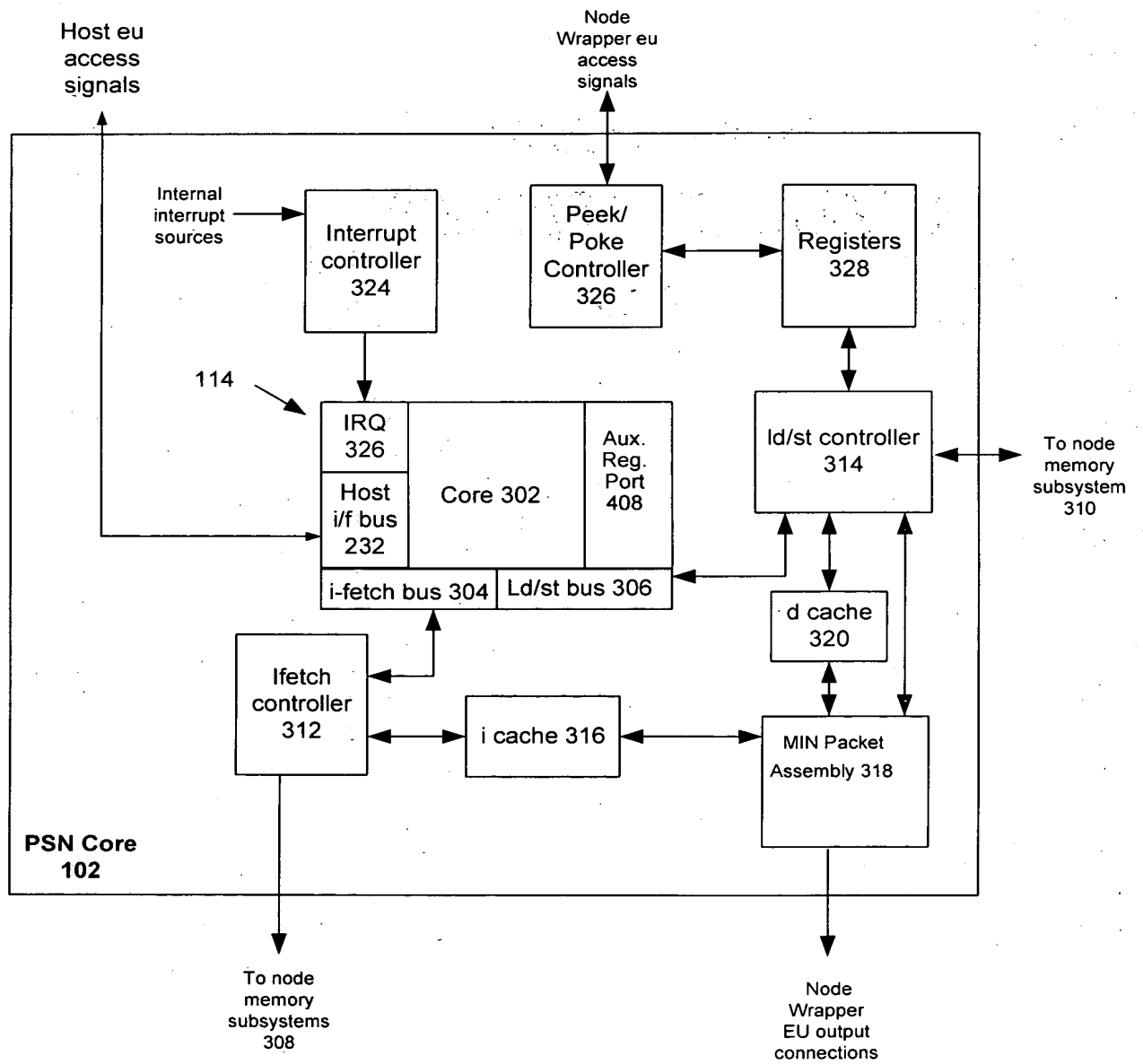


Figure 3

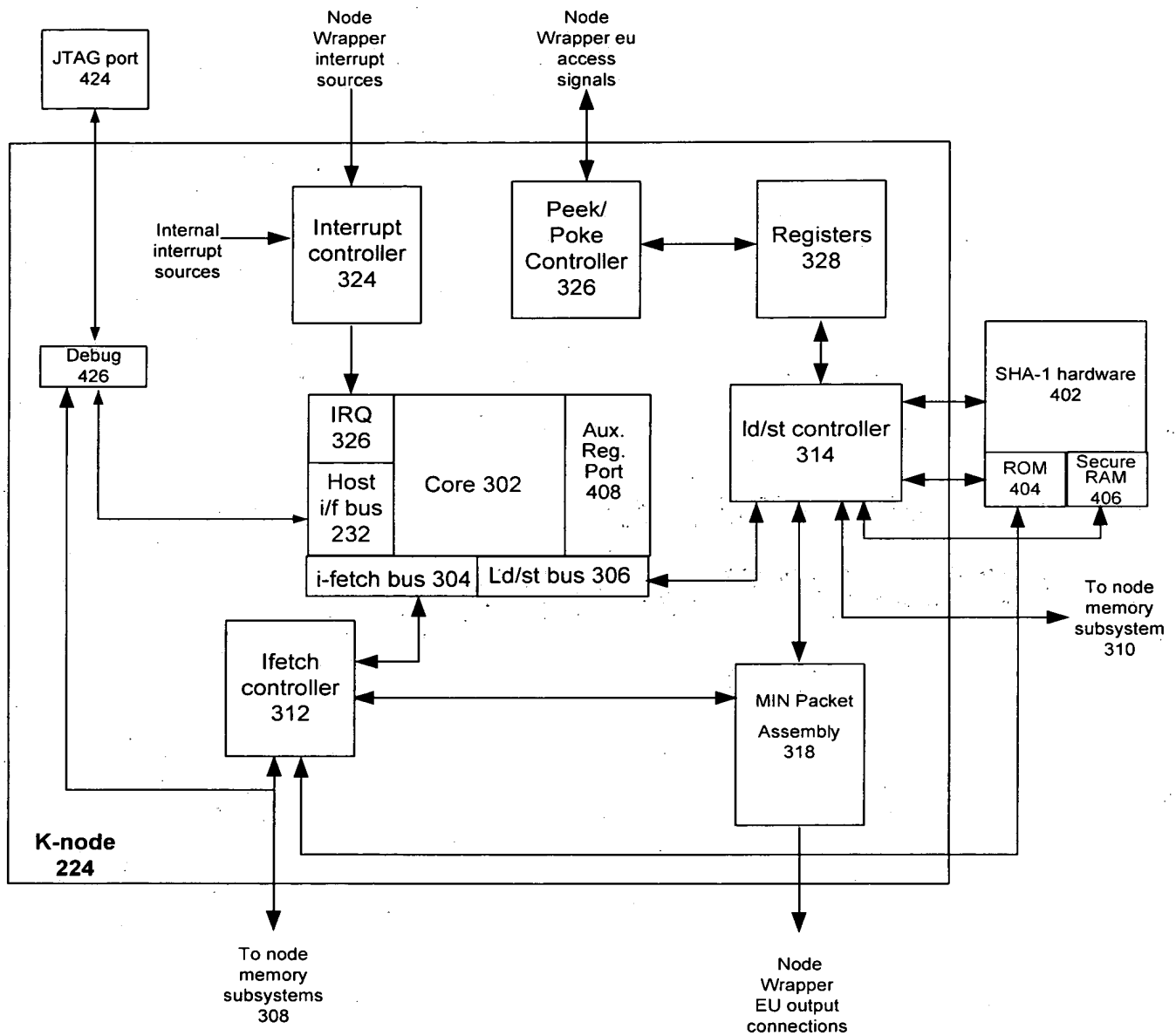


Figure 4

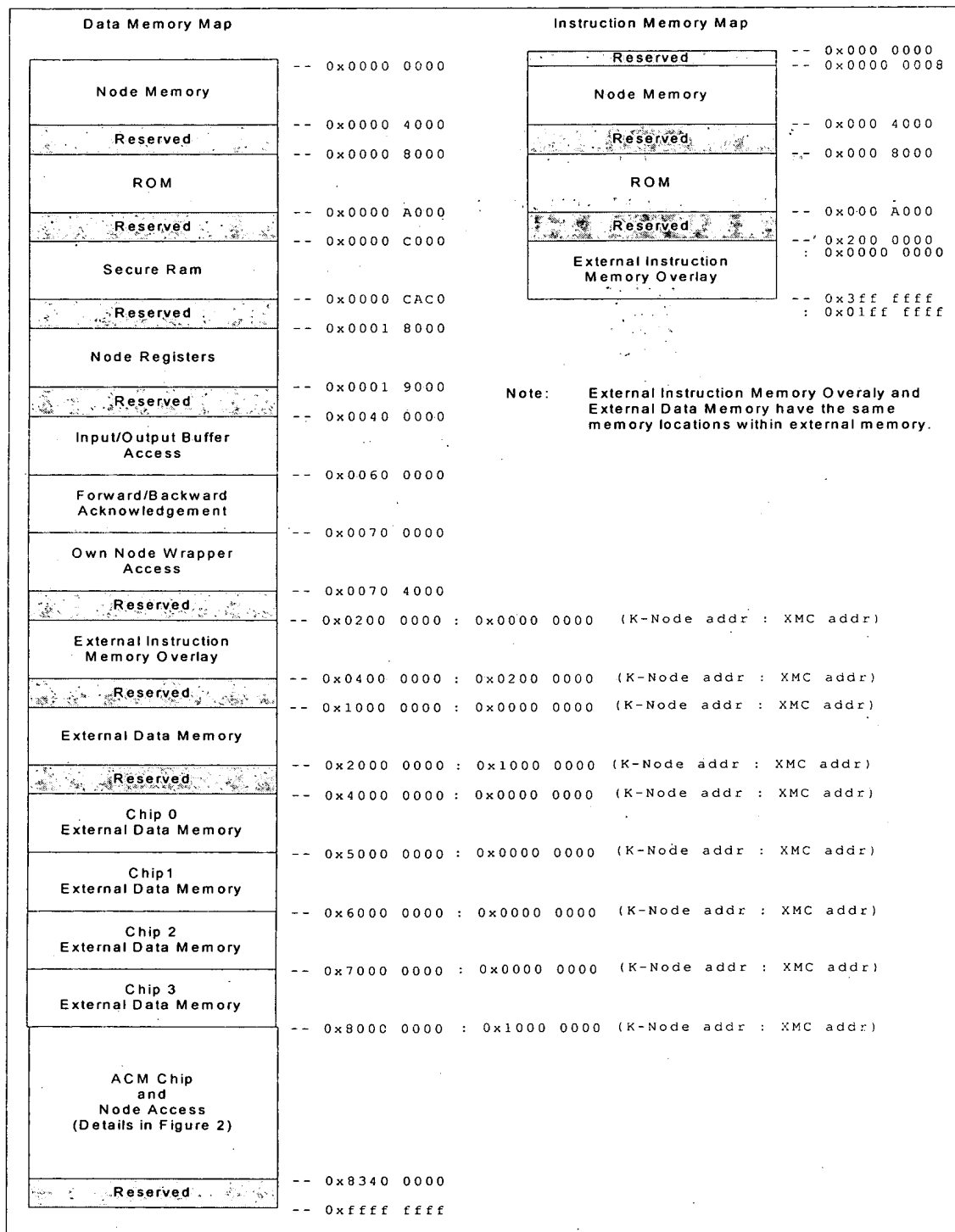


Figure 5A

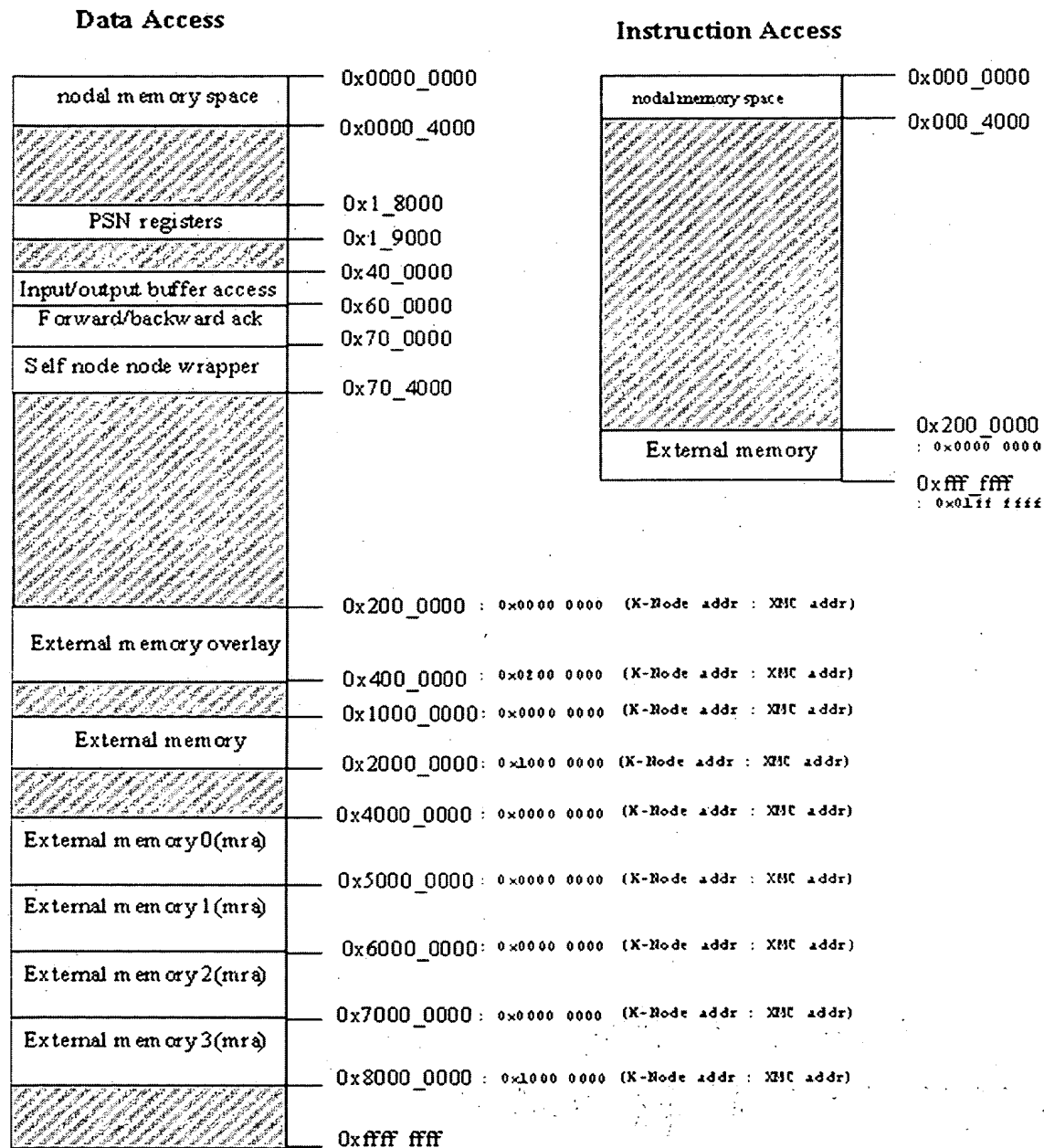


Figure 5B

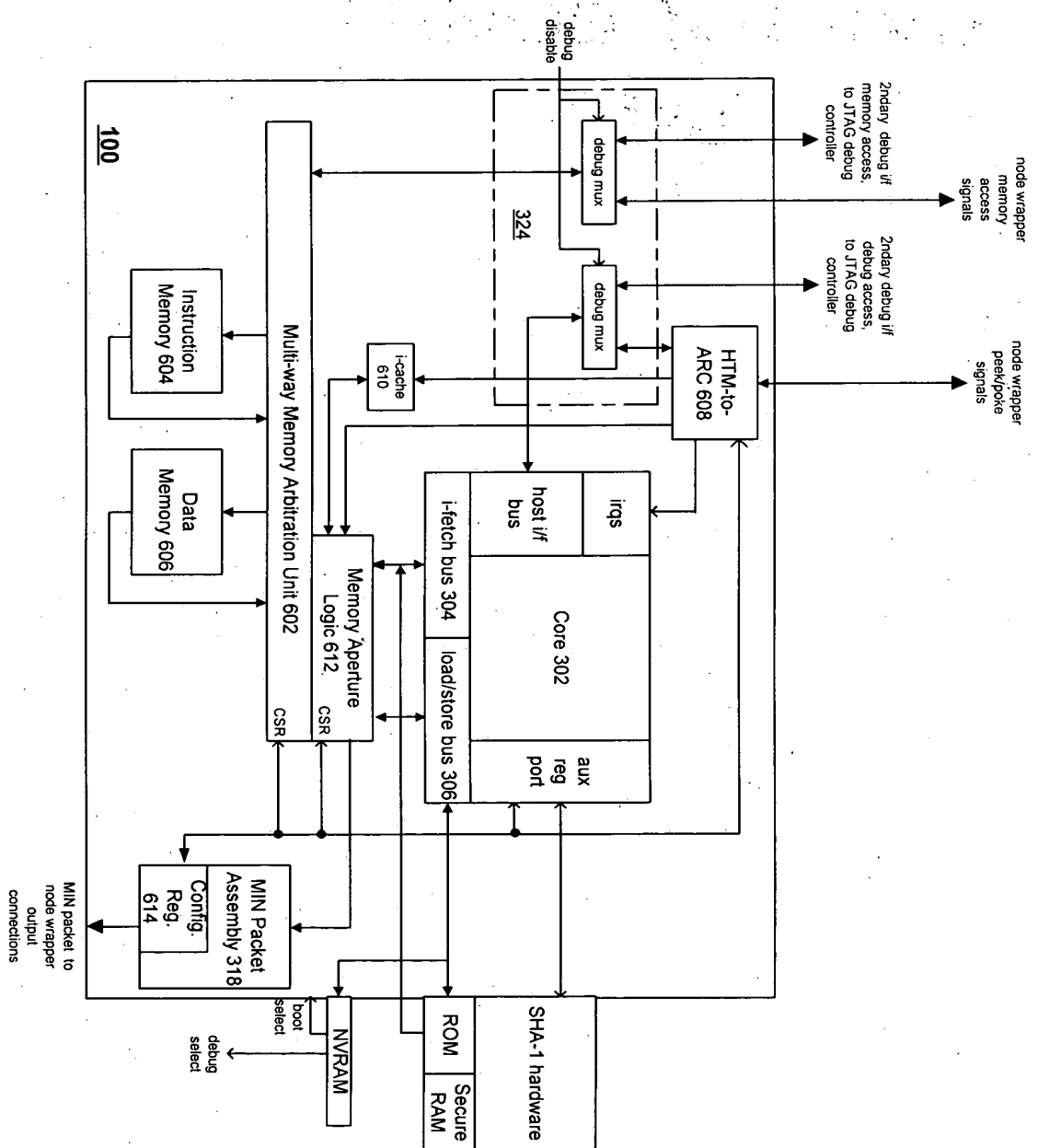


Figure 6

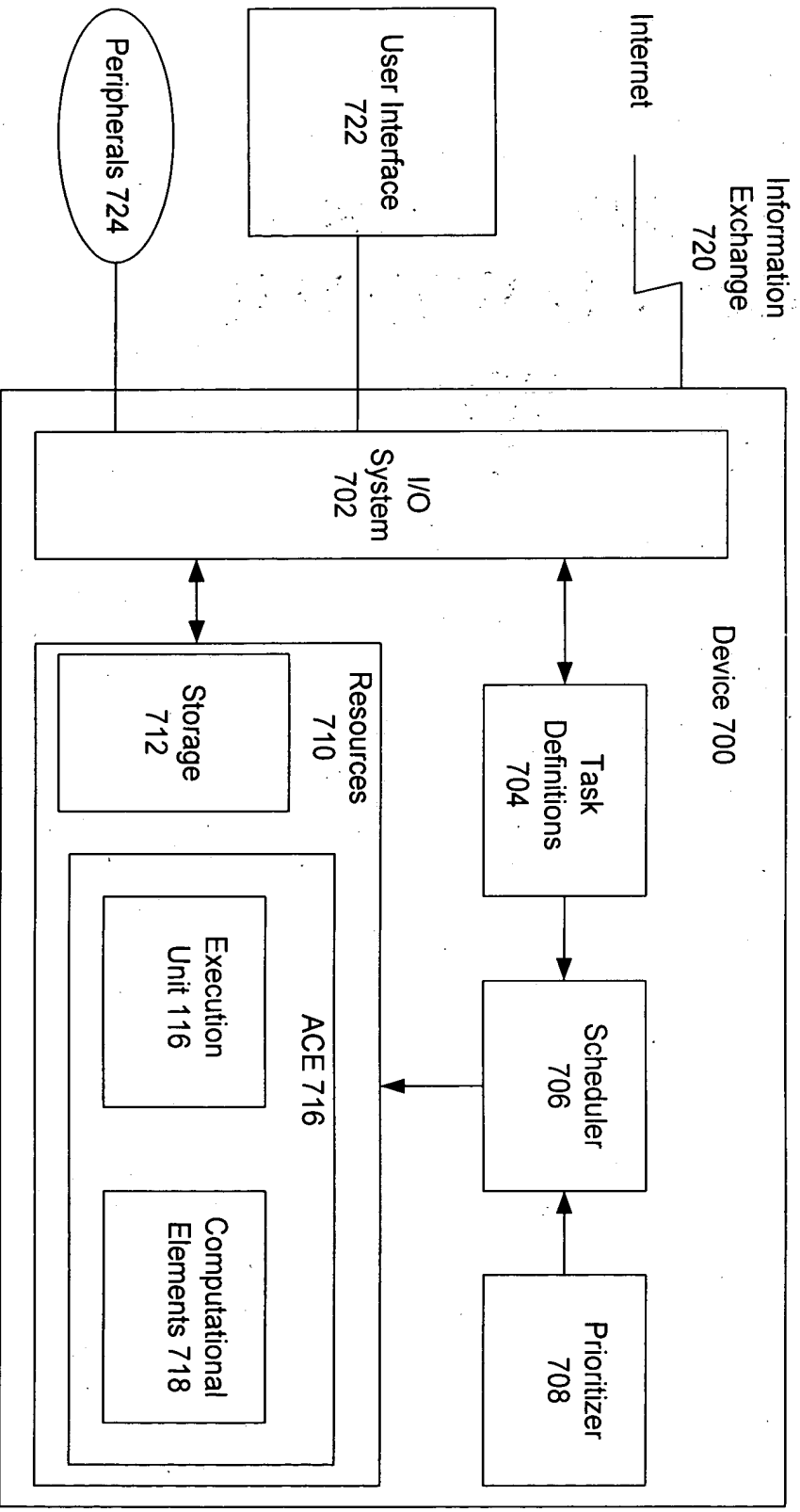


Figure 7

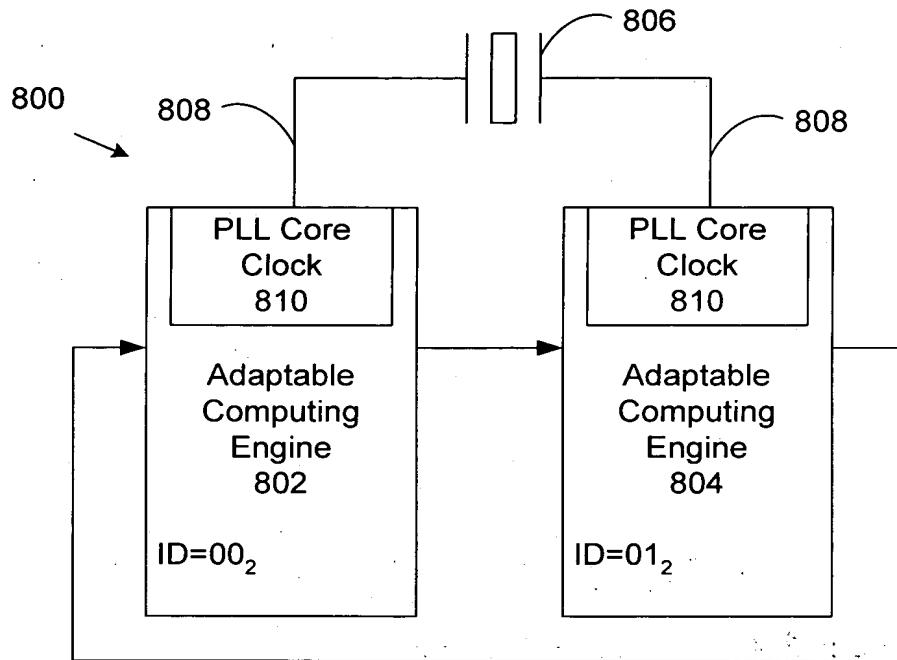


Figure 8A

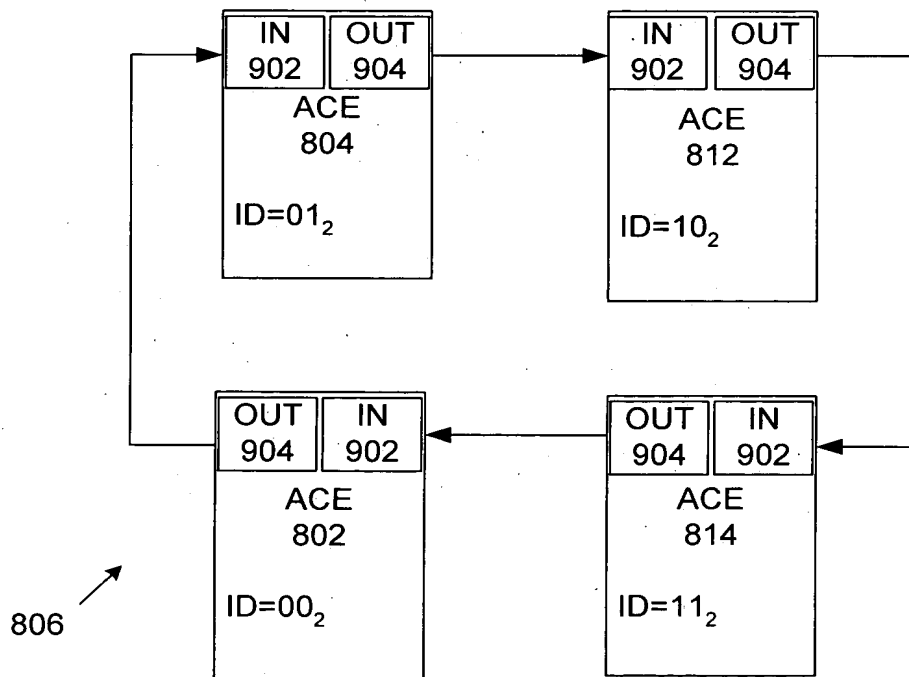


Figure 8B

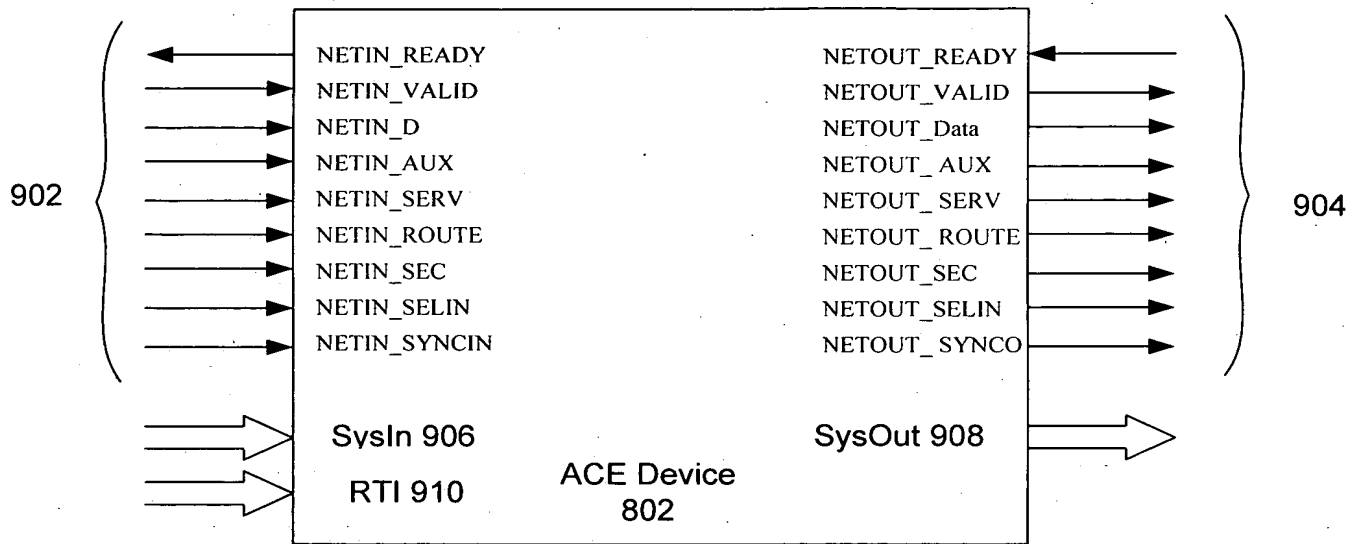


Figure 9

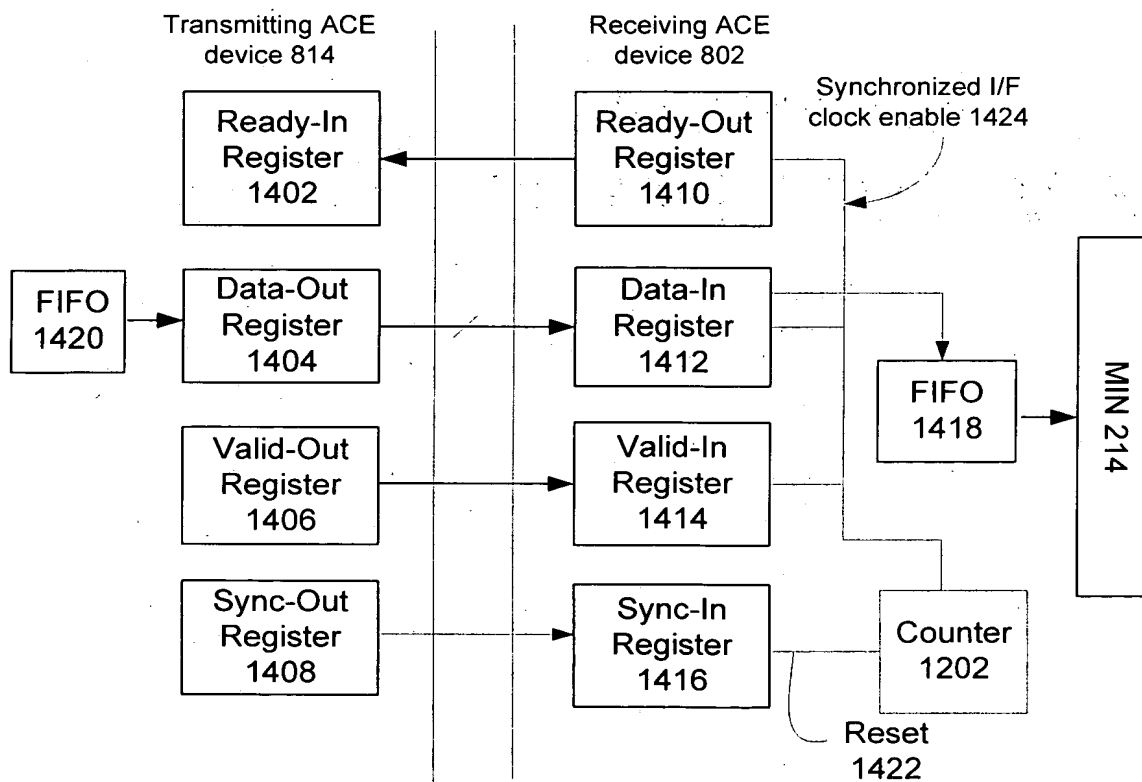


Figure 14

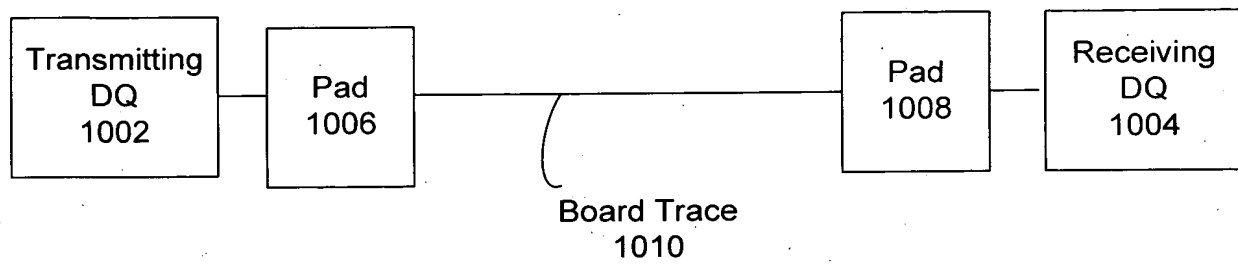


Figure 10

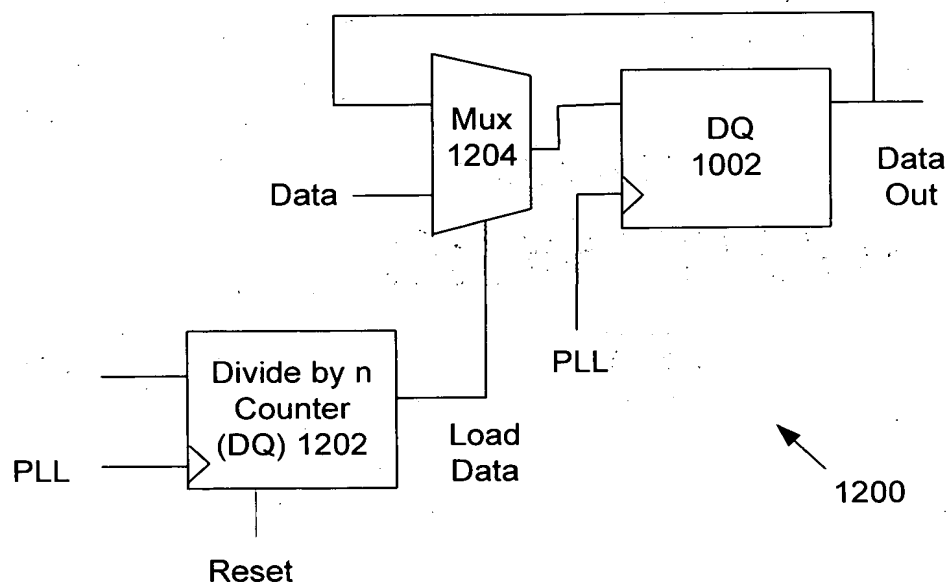


Figure 12

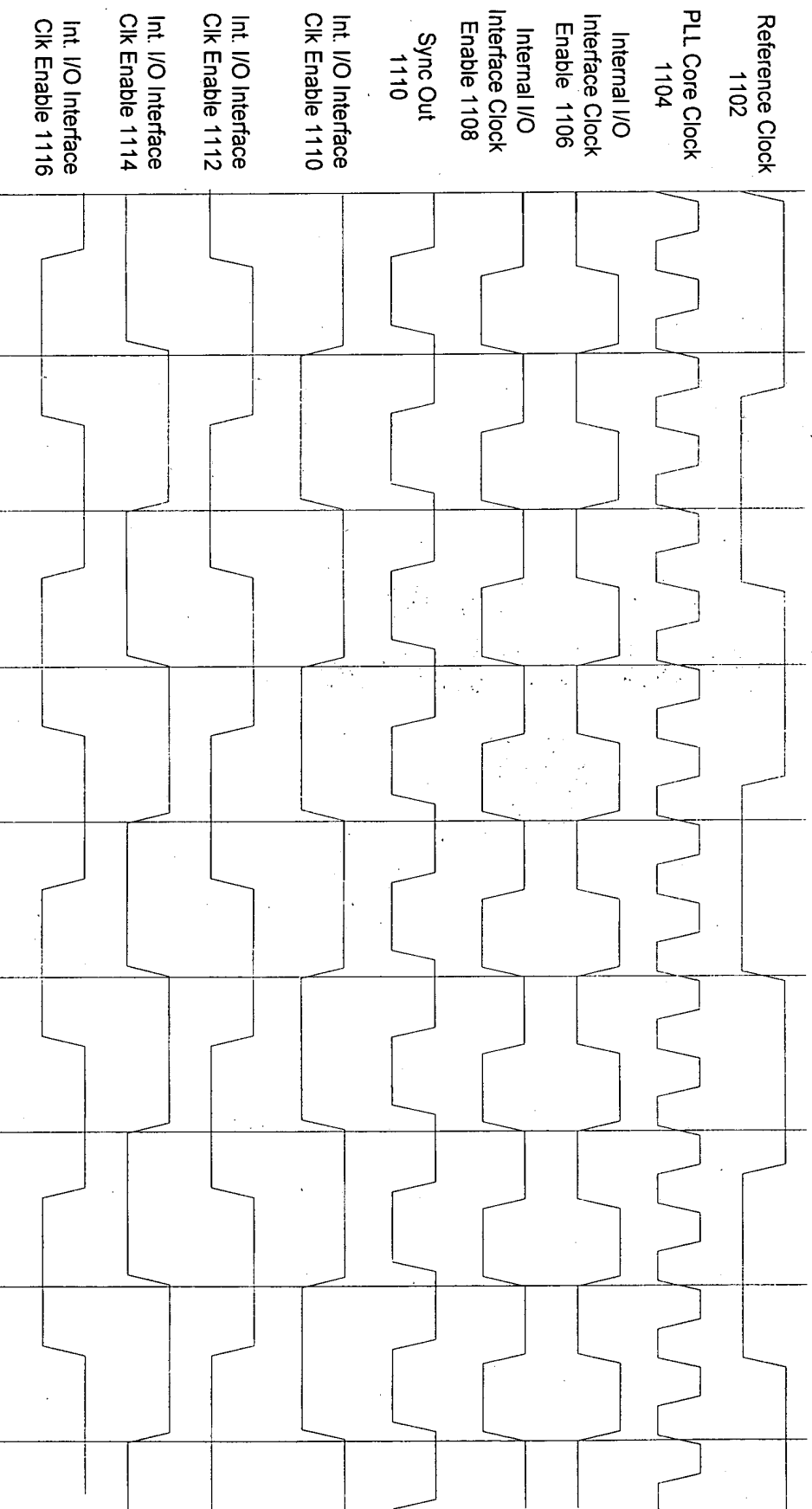


Figure 11

NetIn - NetOut INTERFACE (half-speed interface)

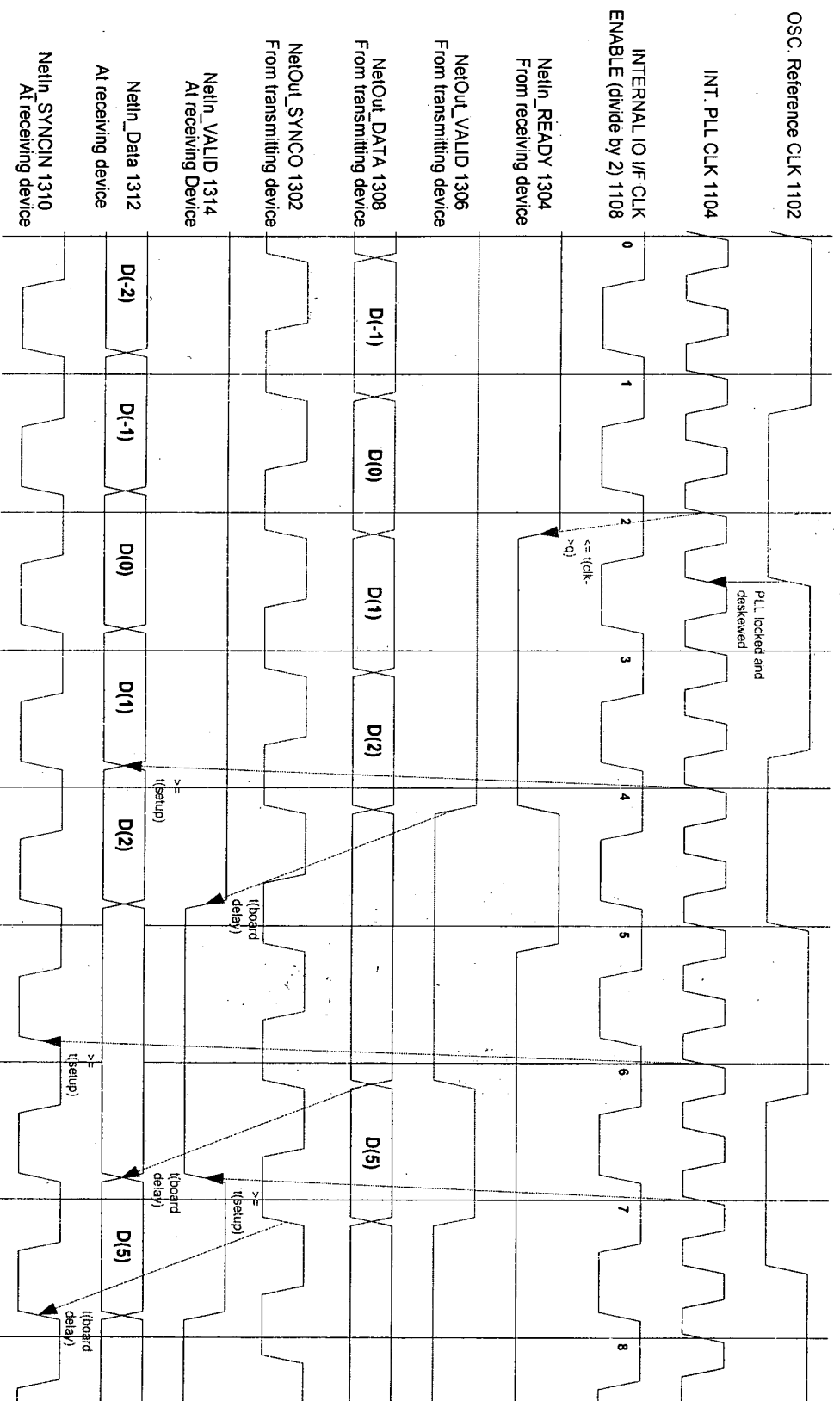


Figure 13A

SYSIN INTERFACE (full-speed)

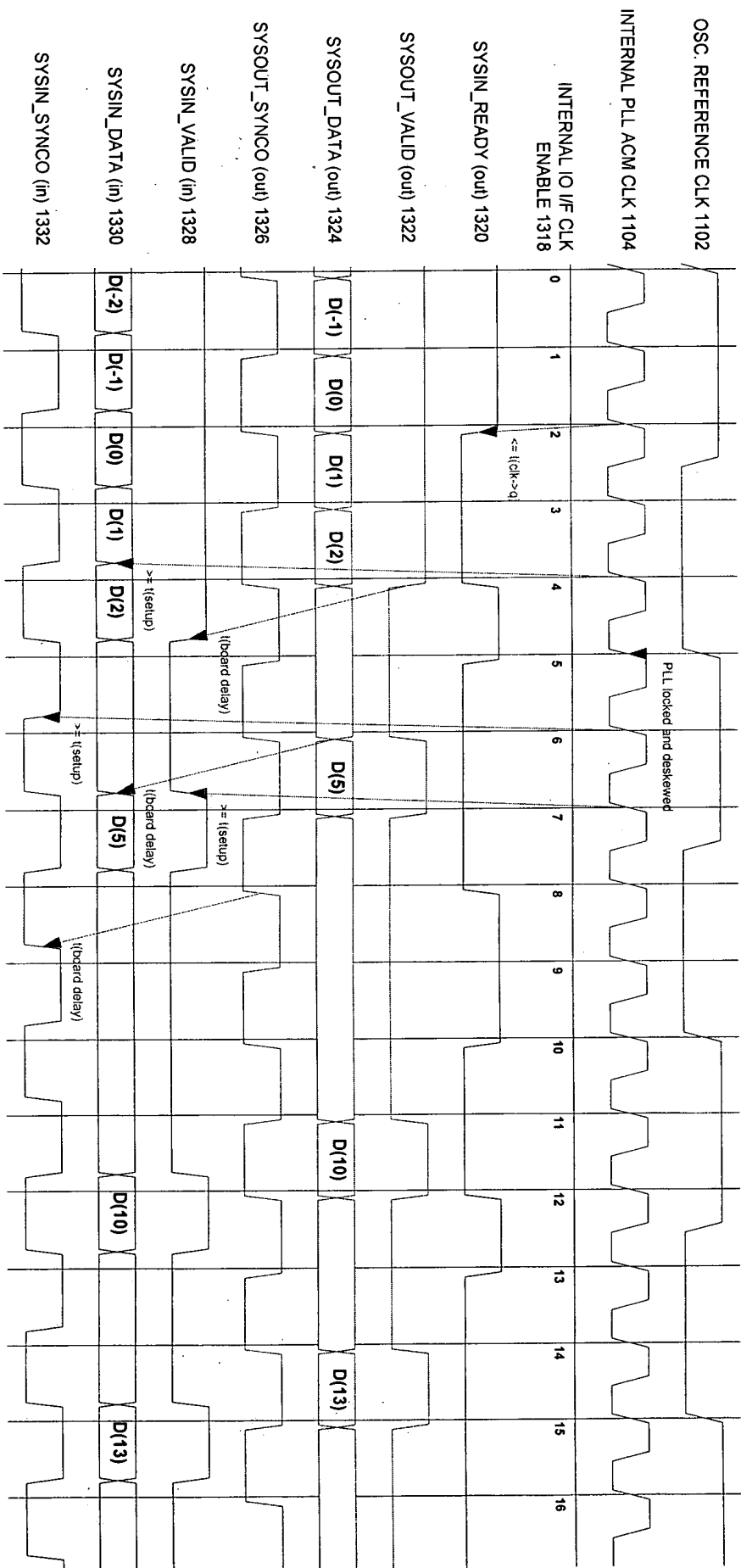


Figure 13B

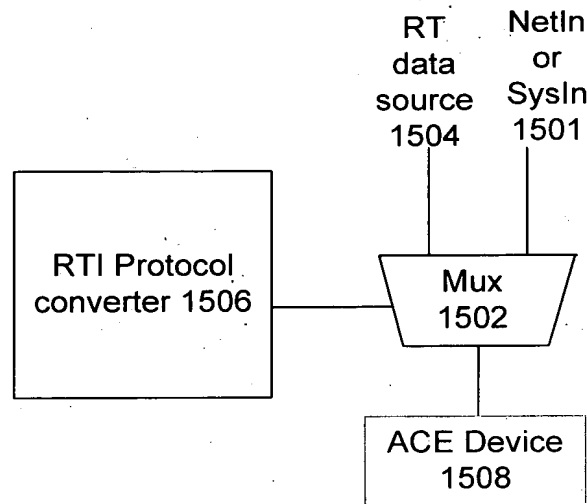


Figure 15

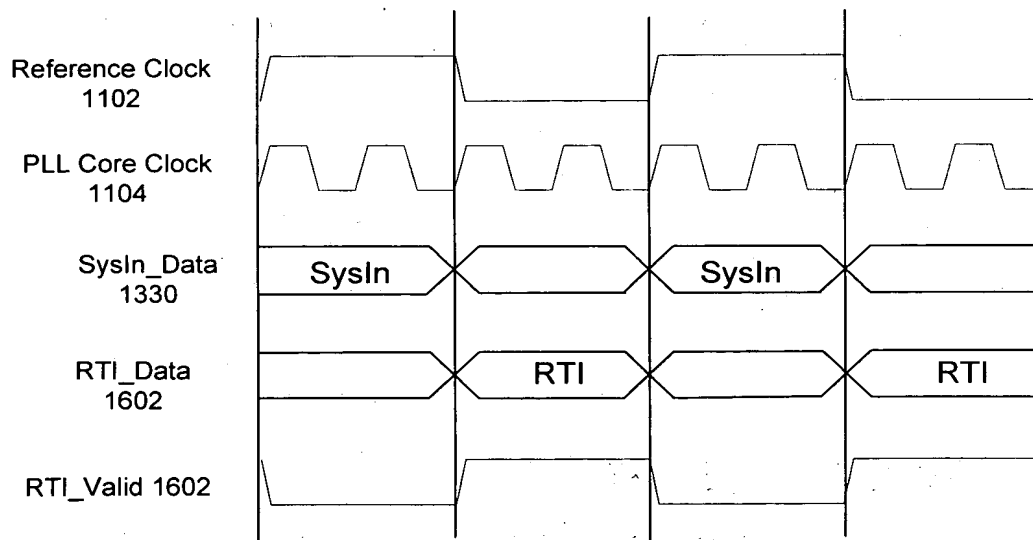


Figure 16